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May 23, 2008

The Honorable Joseph J. Farnan, Jr.
United States District Court
For the District of Delaware
844 North King Street
Wilmington, DE 19801

VIA ELECTRONIC FILING

Re: *ON Semiconductor Corp., et al. v. Samsung Electronics Co., Ltd., et al.*
C.A. No. 07-449 (JJF)
Samsung Electronics Co., Ltd, et al. v. ON Semiconductor Corp., et al.
C.A. No. 06-720 (JJF)

Dear Judge Farnan:

On behalf of ON Semiconductor Corp. and Semiconductor Components Industries LLC ("ON Semiconductor"), we write in furtherance to the *Markman* hearing held this Wednesday, May 21, to provide copies of the two slides that counsel for ON Semiconductor used during the hearing, but which were not part of the binders that were provided. Slide no. 002a is modified to reflect the parties' proposed constructions of "analog trimming" from U.S. Patent No. 5,361,001. The second slide concerns ON Semiconductor's proposed construction of "pins" from U.S. Patent No. 6,362,644.

Respectfully,



Richard J. Bauer (#4828)
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RJB/dlb

cc: Clerk of the Court (Via Hand Delivery)
John W. Shaw, Esquire (Via Electronic Mail)
John M. Desmarais, Esquire (Via Electronic Mail)
Behrooz Shariati, Esquire (Via Electronic Mail)

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“analog trimming” (’001 Patent, Claim 4)

ON Semiconductor Construction	Samsung Construction
modifying an analog value or quantity	fine adjustments of an analog component

- Disputed Issues –
 - Whether preamble is a limitation
 - Alternatively, claim construction issues –
 - whether trimming is limited to analog circuit components
 - whether trimming is limited to “fine” trimming

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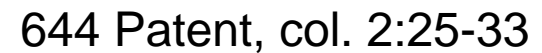


FIG. 5